

Notice of References Cited	Application/Control No. 10/671,773	Applicant(s)/Patent Under Reexamination CHIU ET AL.	
	Examiner Fernando L. Toledo	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,627,403 A	05-1997	Bacchetta et al.	257/639
	B	US-6,235,592 B1	05-2001	Sung, Kuan-Chou	438/278
	C	US-6,242,303 B1	06-2001	Wang et al.	438/257
	D	US-6,346,737 B1	02-2002	Higashitani et al.	257/511
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	K	US-			
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf and Tauber; "Silicon Processing for the VLSI Era Volume 1: Process Technology"; pp. 161 and 168; 1986, Lattice Press; Sunset Beach, CA
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.